The worldwide semiconductor market reached \$137 billion in
total revenues in 1997 (1). The assembly of these semiconduc-
tor devices into value-added electronic components and prod-
ucts represents a major, if not the domi ity in many countries. The introduction of the concept of
product stewardship (2) into electronic products has become
increasingly urgent as consumers realize that most of the
Avoid using materials that cannot be recycled electronics products end up as solid wastes in landfills after • Use materials that do not cross-contaminate each other. the end of their service life (3). An integrated solution must • Use material- and energy-efficient processes and tools. be found to solve the problems associated with the creation of these products; that is, it is important to minimize the envi- Specific examples of these rules will be discussed in the reronmental impacts from production activities that are associ- spective sections. ated with devices design, assembly, and product end-of-life Other important environmental aspects can be addressed management. **beyond assembly level.** Concepts such as design for disassem-

bly processes with an emphasis on processes that produce design that emphasizes waste minimization and product refaster and smaller electronic products such that the goal of cyclability. It is critical to design products that are easy to energy and material conservation can be achieved during and disassemble because this improves the ability to reuse or reafter product assembly. Specifically, the following subjects cycle parts of used or defective products. One important conwill be discussed in detail: (1) processes and materials selec- clusion from the DFE (design for environment) studies is the

tion, (2) pollution prevention methodology, and (3) product end-of-life management.

ENVIRONMENTAL AWARENESS

The need to phase out chlorofluorocarbons (CFCs) and other related ozone-depleting chemicals in the early 1990s triggered an international competition that has since resulted in more than 80% reduction in production of all CFC materials in 1997 (4). Companies working cooperatively with governments have produced many CFC alternatives that also make good profits (5). Encouraged by the dramatic success of the CFC elimination programs of the electronics industry, both the devices and packaged electronic products sectors expect more stringent environmental regulations to be imposed by the government. These proposed regulations may include restrictions on perfluorinated compounds (PFCs) usage (PFCs are a common plasma etchants) because PFCs are extremely stable in the atmosphere and produce heat-absorption effects that are up to 25,000 times more potent than those of carbon dioxide (6). Toxic metals that are currently used in the consumer products are facing similar potential regulations. Specifically, the use of lead in today's electronics assembly may be jeopardized by the past success following the elimination of lead in the paint and gasoline industries. The drastic reduction of lead deposition in rural landscapes far away from polluting sources represents the critical criterion for which the success of the elimination program is judged (7). This result, in conjunction with other environmental movements (8), may justify strict future regulations on toxic metals such as lead, cadmium, and antimony used in various electronic appliances.

It has been demonstrated repeatedly that an environmentally sound manufacturing process can also be a profit-making one (5). Miniaturization of electronic packages and process simplification are the keys to profits, in addition to waste minimization and efficient recycling. Miniaturized packages such as chip-scale packages (CSPs) (9) occupy much less (only 20%) footprint (area on the circuit board) as compared to con-**ENVIRONMENTALLY SOUND**
ASSEMBLY PROCESSES
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This article covers current environmentally sound assem- bly (DFD) (10) provide guidelines for environment-friendly

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necessity of minimizing the number of mechanical fasteners, little energy consumption and little waste production in the such as screws and rivets, on the circuit boards that may in- treatment processes. Flat, planar surfaces that are ideal for terfere with product recycling. Elimination of lead-containing surface mount technology (SMT) assembly give the OSPsolder, reduction of PFC and hazardous air pollutant (HAP) treated boards advantages in the fine pitch applications. emission, and reduction of energy consumption in integrated However, all OSP boards are prone to air oxidation at tempercircuit (IC) manufacturing have all been proposed in the 1997 atures above 100C. Selection of proper aggressive fluxes, issue of *National Technology Roadmap for Semiconductors* along with reflow in nitrogen, is required in order to produce *Technology Needs* (11). consistently robust solder joints by using the OSP boards.

Flux ENVIRONMENTALLY BENIGN MATERIALS SELECTION

that will minimize impacts on the environment during assem-
bly. Materials selected should allow (a) hazard-free handling residue Advantages of rosin-based flux include: good fluxing bly. Materials selected should allow (a) hazard-free handling residue. Advantages of rosin-based flux include: good fluxing during assembly and (b) ease of recycling and disposal at the properties tunability (producing dif during assembly and (b) ease of recycling and disposal at the properties, tunability (producing different activity grades by end of product life.

The assembly process begins with a patterned substrate of ties) (18) or water-soluble flux (19) is to replace the current
eighter a ceramic substrates or mutilayer fiberglass-reinforced from LSF
epaxy board. Ceramic subst

these exposed copper structures. These include solder [hot air this solder-based finish does not meet either the coplanarity other metallic finishes are either too new to establish reliabil-
ity statistics (for palladium) or not cost-effective (for nickel/ phor thermally dissipate during the reflow operation. ity statistics (for palladium) or not cost-effective (for nickel/ gold), and also they generate more processing waste than the The goal of soldering joints without flux can be realized by provide a cost-effective way to protect the circuit boards with cess has been applied successfully to flip-chip bonding of

Traditional rosin-based fluxes have been the major flux mate-An assembly process starts with proper selection of materials rials on the market before the Montreal Protocol called for that will minimize impacts on the environment during assem-
the elimination of Freon-113, the cleani simple formulation changes), and electrolytically stable and encapsulating characteristics (prevent reoxidation of solder **Circuit Boards** and ionic migration). If a low solid flux (LSF, 2% nonvola-

environment (temperature and humidity) so that excessive **Surface Finish** humidity does not spoil the flux in the required work shift The patterned copper structures such as bondpads and vias can be challenging. Water has a much higher heat of vaporthat exist on the circuit boards are prone to air oxidation if ization, and therefore the normal reflow profile may not work not protected. Various surface finishes are available to protect without significant modification. A successful VOC-free LSF
these exposed copper structures. These include solder [bot air using a simple mixture of 3% adipi solder leveled finish (HASL)], organics [organic solderability demonstrated in the literature (21). A third category of the preservative (OSP)], nickel/gold, and palladium. HASL is still environmentally friendly flux is the thermally dissipated flux;
the largest volume surface finish in use today (14). However, that is, the flux volatilizes cle the largest volume surface finish in use today (14). However, that is, the flux volatilizes cleanly after reflow, thus leaving
this solder-based finish does not meet either the conlanarity no residue on the circuit board. requirements for current fine pitch input-output (IO) leads as- proach by using a mixture of an organic activator (adipic sembly or the potential lead restrictions in the future. All acid), a flux base (camphor), and an organic diluent. The cam-

OSP finishes (15). OSP treatment involves only three simple a plasma process called plasma-assisted dry soldering (PADS) steps: surface cleaning, immersion, and drying. OSP protects (23). The process involves a principle similar to the effect of the exposed copper from oxidation by forming organic copper OSP to the copper; that is, it coats the solder with a comcomplexes (16) on the copper surface that effectively prevent pound that volatilizes at reflow temperatures. A PADS reacfurther oxidation. The copper complexes decompose or volatil- tor uses plasma discharge in a chamber filled with fluorineize at reflow temperatures, thus rendering an oxide-free cop- containing gases such as CF_4 or SF_6 to convert the tin oxides per surface for soldering. Substituted benzotriazoles and im- into tin oxyfluoride, which volatilizes to reveal a fresh, oxidemidazoles are the two most commonly used OSPs (17). They free solder surface for joining during reflow. This fluxless pro-

Figure 1. TGA thermogram of a typical no-clean solder paste, which shows a solder content of 93.2%. The two peaks shown on the left indicate the temperatures at which the weight loss occurs rapidly.

light-emitting diode (LED) devices that require extremely **Lead-Free Solder Paste** high optical transmission for display applications (24). The
negative side of this process is that both CF_4 and SF_6 are
greenhouse gases that cause global warming (6).
The obvious technique to eliminate lead in the el

screen amount of nonvolatiles after simulated reflow and (b) tests mentioned in the previous section. the copper mirror test to screen corrosiveness; these methods are effective in selecting the right pastes. Figure 1 shows a **Encapsulant** typical TGA thermogram of a no-clean solder paste that contains a solder content of 93.2%. For fine-pitch printing, type There are many different types of encapsulants available to teristics of different types of solder pastes. quite similar in chemical composition. All consist of a mixture

Solder Paste Solder Paste most all of these alloys for paste formulation is only the first step. Almost all of these solders contain high concentrations of tin, Five common ingredients constitute a standard solder paste: some of which are almost pure tin (e.g., Sn/Ag 96.5/3.5, Sn/ a solder alloy, a flux, an activator, a viscosity modifier, and Sb 95/5), which may promote extensive intermetallic formaa solvent. For no-clean pastes the activator should not leave tion with nickel and other barrier metals (27). The selection corrosive residue that may degrade product reliability, nor ex- of correct lead-free solder paste for a particular assembly processive inert residue that may interfere with product electri- cess is a complex process that involves proper design of barcal testing. Two quick routine paste testing methods (25) in- rier metallurgy and boards that withstand the reflow temperclude (a) the thermal gravimetric analysis (TGA) (26) to ature, in addition to the standard solder paste performance

5 (20 μ m to 25 μ m) solder powder at a metal loading of the electronics industry, and molding compounds occupy the greater than 88% weight produces the best results. Most sol- bulk of the current packaging market. Other encapsulants der pastes use ethyleneglycol ethers as solvent. Being a VOC such as glob tops and underfills occupy only a small fraction and reproductive hazard, other more benign solvents such as of the application. Although quite different in their physical propyleneglycol ethers will start to appear in the paste formu- forms (molding compounds are in solid flakes, while glob tops lations. Table 1 presents a list of critical performance charac- and underfills are liquid in syringes), these encapsulants are

Table 1. Critical Performance Characteristics of Different Types of Solder Pastes

Solder Paste Type	Printability	Slump	Solder Balling	Tackiness	Wetting	Profile Window	Reflow Atmosphere	Work Life
Standard	Good	Good	Good	High	Good	Wide	Air	Long
No-clean	Medium	Medium	Good	Medium	Medium	Medium	Air/nitrogen	Long
Low solid	Poor	Poor	Poor	Low	Poor	Narrow	Nitrogen	Medium
Water-soluble	Poor	Poor	Poor	Low	$_{\mathrm{Poor}}$	Narrow	Air/nitrogen	Short

with the bulk being a silica filler. Molding compounds use a adhesives an advantage over other epoxy-based materials semisolid curing agent (phenol novolak) and a higher filler when low moisture within the package is critical. loading (80% to 90% of silica), resulting in a solvent-free mix- The final forms of the adhesives can be found in premixed ture that cures without VOC emission. The length of the time liquid syringes, B-staged tackiless film, or bulk two-part for a molding compound to cure is directly proportional to the (resin and curing agent) jars. Syringes and films are more energy consumption of the molding operation, hence the envi- effective in terms of material utilization, but the two-part ronment impacts. A molding compound that cures faster is a packages are convenient for long-term room temperature preferred choice. storage.

The presence of liquid curing agents or solids that are dissolved in reactive solvents in the underfills and glob tops are the keys in differentiating them from the molding compounds. **ASSEMBLY PROCESS CONSIDERATIONS** Typical liquid curing agents include hexahydromethyl phthalic anhydride (HMPA), nadic anhydride (NDA) (28), and An environmental-friendly assembly process should primarily *N*-cyanoethyl-2-ethyl-4-methyl imidazole (EMIN). Reactive focus on reducing the number of steps to achieve maximum diluents are solvents that also participate in epoxy polymer- product yields by consuming the least amount of energy. Secization. Common reactive diluents used in underfills include ondary concerns such as waste minimization and product rephenyl glycidylether and γ -butyrolactone. HMPA-containing cyclability are also important. A typical circuit board assemencapsulants require an intermediate soaking step in curing bly line consists of automatic pick-and-place robots that are so that this volatile compound can be anchored on the poly- fed by bumped die or device packages mounted on tape and mer chain first before fully cure can occur. Contrary to reel or on waffle packs in either an SMT format or a through-HMPA, the EMIN-containing underfills can be ''snap-cured'' hole interconnect (THI) insertion format. The bare circuit without emitting a significant amount of VOC, which makes boards are carried by a conveyor belt to an in-line dispenser this type of encapsulant more environmentally friendly. that is capable of depositing the following three different ma-

circuit boards assembly are (a) components anchoring and (b) ity and amount of the material to be dispensed. Bumped die
interconnection. Component anchoring using adhesives are or other device packages are then picked, pla common practices in heat spreader attachment and through- or inserted (for THI) in the selective board areas where the hole device attachment before wave soldering. Examples of dispensed materials are located. A thermal treatment that readhesive interconnects are polymer flip chip (29) and aniso- flows the solder bumps to form the device-board interconnects tropic conductive film (ACF) interconnects (30). Advantages or that cures the adhesive to hold the devices on the board of adhesive interconnects include simple operation and envi- follows the components placement or insertion. For THI deronmental soundness due to the absence of lead alloys. ACF vices a new cycle of flux application and wave soldering
interconnects are self-encapsulated (i.e., no underfill opera-
(boards pass over flowing molten solder poo tion is needed), and therefore they can be the most environ- through-hole joining process.

conductive coatings on the spheres. Common resins used in nol F, and novolak epoxides. EMIN and other substituted im- joints (32). idazoles are common curatives in the adhesives. Bisphenol The continuing shift of the electronic assembly processes F-type resins develop superior adhesion to FR-4 boards and from THI to SMT in the past decade has produced more than solder masks, making them a preferred choice. The cured cya- 60% of the market share for SMT-assembled packages (14). nate esters form three-dimensional dendritic triazine cage Process simplicity is the key to the success: it involves only structures (31) that also serve as a built-in desiccant in her- three steps: print, place, and reflow. Surface mount technol-

of an epoxy resin, a curing agent, and an adhesion promoter, the package. This unique property gives cyanate ester-based

terials on selective areas of the board: a solder flux, a solder **Conductive Adhesive**
 Conductive Adhesive
 Conductive Adhesive serves in the nature of the assembly processes. The dispenser can be a
 Conductive adhesive serves in the meumatic pump or a screen printer depending on pneumatic pump or a screen printer, depending on the viscosor other device packages are then picked, placed (for SMT), (boards pass over flowing molten solder pool) completes the

mentally friendly type of assembly process to use. After solder joining, an encapsulation process (underfill,
Two types of resin polymers form the backbone of adhe-
 $\frac{1}{2}$ aloh top, or overmold) is then performed to pro Two types of resin polymers form the backbone of adhe- glob top, or overmold) is then performed to protect the sives for assembly: epoxies and cyanate esters. The standard mounted bare die in the direct chip attach (DCA) p mounted bare die in the direct chip attach (DCA) process. Enformulation involves mixing 50% to 70% of silver flakes into capsulation processes that are time-consuming must be optia resin-curing agent mixture to form the isotropic conductive mized or eliminated entirely by materials improvement or adhesives. The ACF uses 1% to 5% of conductive spheres in a process changes. This can normally be achieved by using similar resin-curative mixture to cast a B-staged (50% cross- shap-cure epoxy as encapsulants or by applyi snap-cure epoxy as encapsulants or by applying a process that linked) film of 25 μ m to 100 μ m thick. Both silver flakes and does not require encapsulation. For those prepackaged compo-
conductive spheres serve to conduct electricity. The aniso-
nents such as ball grid arrays (conductive spheres serve to conduct electricity. The aniso-
tropic nature and the low metal loading in the ACF prevent tion is not normally required, and therefore significant protropic nature and the low metal loading in the ACF prevent tion is not normally required, and therefore significant pro-
it from being an effective heat conductor, and this precludes cess simplification is achieved. Figure cess simplification is achieved. Figure 2 shows the simplified ACF from being used in applications that require high ther- assembly process flows for three major assembly operations mal conductivity. Conductive spheres are made from electro- (DCA, SMT, and mixed technologies). All cleaning steps after less plated glass spheres or divinylbenzene–cross-linked poly- reflow or wave soldering have been removed to conform to styrene beads. Both silver and nickel/gold have been used as environmental friendliness. This can be done by using noclean solder pastes in a high-purity nitrogen $\left(\langle 20 \rangle$ ppm oxyconductive adhesives include bisphenol A diepoxide, bisphe- gen) reflow oven to ensure reproducibility of reliable solder

metic packages to maintain an extremely low moisture within ogy is a good example of the "green" assembly process that

Figure 2. Simplified assembly process flows for three major assembly operations [DCA (direct chip attach); SMT (surface mount technology), and SMT-THI (surface mount technology–through hole in- **Figure 3.** A process flow for the ACF-based bare die packaging asterconnect)]. SMD (surface mount device); THD (through hole device). sembly.

uses less material by increasing package interconnect densi- The solder bonding process is self-aligned; that is, the de-

underfill-free adhesive-based processes are anisotropic conductive film (ACF) (30) assembly process and area bonding **Flip-Chip Advantages** conductive (ABC) (33) adhesive interconnect process. ABC is

ties through array-type IO leads. Ball grid arrays, especially vice-board interconnects are surface-tension-controlled so plastic ball grid arrays (PBGAs), for example, have become that even with some degree of bump-pad misalignment (up to the standard SMT components. To shrink the area of array 30%) the assembled parts still exhibit good joining after reinterconnect components further, CSP inevitably becomes flow. This self-repairing nature creates a larger processing SMT's next logical target. Table 2 presents a comparison window for solder assembly than those of adhesive-based proamong the SMT, THI, and DCA assembly processes. cesses. However, the need to maintain a solder-wettable sur-Certain polymer-based bare die assembly processes do not face in the metallurgical bonding does impose major conrequire post-bonding encapsulation, and thus they provide po- straints on the process—mainly the presence of flux to render tential solutions to the slow encapsulation problem. The two solderability, and its removal after it has served this function.

a special form of ACF that uses area-patterned conductive To fully utilize the advantages of SMT and its potential enviand non-conductive epoxy to form interconnects and encapsu- ronmental benefits, all components to be assembled should be lation simultaneously during bonding operation. Figure 3 SMT-compatible. The smaller-footprint SMT-compatible describes a process flow for the ACF-based packaging as- packages such as CSPs are on the focal points. Two different sembly. device-carrier interconnect methods are in the mainstream Key to the success of all adhesive interconnects is a stable CSP design: flip chip and wirebond. Examples of the CSPs metallic bonding surface on the top of the aluminum bondpad, are represented by Motorola's JACS-Pak (flip chip) (9) and whether it is coated with metal finishes (e.g. Ni/Au, or Ti/W/ by Tessera's μ BGA (wirebond) (35). Although wirebond offers Au) or, more preferably, by a simple maskless chemical pro- simplicity and maturity of the bonding technology to produce cess to preserve the Al surface from oxidation. One such ap- effective device-carrier interconnects, serious performance proach is by using a cyanate ester-based adhesive in combina- limitations do exist in this approach. High package parasitics tion with palladium/chelate aluminum surface treatment (34) (both inductance and capacitance) impair wirebond devices' to form electrically reliable bumps on aluminum bondpads. performance (36) as well as larger footprint and longer in-The right side of Fig. 4 represents a process flow for this sim- terconnect distances in the final packages, which also demand ple bondpad preservation process. higher power consumption. All these drawbacks bear conse-

Table 2. A Comparison Among the SMT, DCA, and Mixed Assembly Processes

Process	Number of Process Steps	Interconnect Density	Material Utilization	Disassembly	Solder Recyclability
SMT^a		High	High	Easy	Good
DCA^b		High	High	Difficult	Medium
Mixed		Low	Low	Difficult	Poor

^a SMT, surface mount technology.

^b DCA, direct chip attach.

Figure 4. A process flow of an electroless nickel/gold (left) and polymer (right) wafer bumping.

quences that can have negative impacts on the environment. $(>\!\!90\%)$. The electroless bumping involves maskless deposi-

process, which is an abbreviation for controlled collapse chip connect, was invented 30 years ago. The original process uses **Solder Jetting.** This is a wirebonder equivalent of the flipa molybdenum shadow mask to define a sequential vapor de-
position of a high lead allov (95% Pb and 5% Sn) onto device the feasibility of controlled generation of monodispersed molposition of a high lead alloy (95% Pb and 5% Sn) onto device the feasibility of controlled generation of monodispersed mol-
bondpads. Due to a long trajectory (for uniform deposition) ten solder droplets. Based on a simila bondpads. Due to a long trajectory (for uniform deposition) and small apertures (for fine pitch bondpads) of the mask, of an ink-jet printer, molten solder droplets are forced out solder utilization rarely exceeds 0.1%. The evaporative nature through capillary tubing by a piezoelec solder utilization rarely exceeds 0.1%. The evaporative nature through capillary tubing by a piezoelectric actuator and de-
of the process also carries a poor energy efficiency tag. The posited on the UBM. Oxide formation of the process also carries a poor energy efficiency tag. The need to clean evaporation chamber, mask, and wafer holder molten solder jet was shown to have a drastic effect on the accessories and subsequent disposal of the lead-containing droplet formation process, thus reflecting th accessories and subsequent disposal of the lead-containing wastes make this bumping process one of the least environ- atmospheric control in the yield of bumps formation. The mentally sound bumping processes. Other bumping processes UBM can be of any type of solder-wettable surfaces—for exhave also been developed, including E3 (evaporated extended ample, gold, nickel/gold, or organic solderability preserved eutectic) bumping (38), which is a variant of the C4 and hence copper. Solder-jet wafer bumping eliminates all solder print-

ing and electroless bumping/solder print (39) are the two der optimum jetting conditions. However, the durability of the more environmentally friendly bumping techniques. Both pro- jetting head must be improved significantly before this tool cesses can reach a high degree of solder deposition efficiency can be used in production.

Fortunately, performance, power consumption, and material tion of electroless nickel as a diffusion barrier, and a flash of utilization can be improved significantly if flip-chip intercon- Au is deposited subsequently by immersion of the wafer in a nects are incorporated in the package design. cyanide-free electroless gold sulfite bath (40). The bulk of the Two major flip-chip material approaches currently exist: flip-chip bumps are formed by stencil-printing of solder on top metallurgical and polymeric. The metallurgical flip-chip pack- of the Ni/Au under bump metallurgy (UBM). Due to the ages use solder bumps to connect devices to the substrates, maskless nature, coating, developing, and stripping processes while polymeric flip-chip packages use conductive adhesives are all but eliminated in the bumping operation (41). The to serve the same function. combination of the ability to choose various nonlead solder compositions and efficient metal deposition with cyanide-free **Wafer Bumping gold baths makes the electroless bumping one of the most gold** baths makes the electroless bumping one of the most The prerequisite for an environmentally sound metallurgical flexible and environmentally friendly flip-chip bumping pro-
flip-chip assembly process is an efficient wafer bumping processes available. The left side of Fig. 4

has similar undesirable environmental impacts. ing operations, which include stencil fabrication, solder paste application, stencil cleaning, and associated hazardous waste **Nonevaporative Wafer Bumping.** Electroplate solder bump- disposal. Solder utilization efficiency can be close to 100% un-

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volves stencil-printing of wafers with silver-filled conductive cleaning is designed to work. epoxy or cyanate esters under proper squeegee pressure, Other nonsurfactant-containing aqueous cleaners using snap-off, and printing speed. Cure is simple and fast, and it ozonated water as the cleaning chemical has also been renormally does not need the complicated profiling such as that ported (48). Cleaning performance judged by residual metals required by solder pastes. High bumping yields (>99%) have and particles on the substrate surfaces was found to be better been achieved for small-pitch (150 μ m) and low-bump-height than the standard ozone-free mixture. This cleaning method $(<$ 40 μ m) devices (43).

The assumption that circuit-board cleaning after reflow is a
non-value-added operation is now subject to debate (44) . The
primary incentive in promoting a no-clean process was a *reac*-
tive response to the Montreal Pro gressive CFC elimination agenda. As more and more cost-effective cleaning technologies become available to the industry, **Solvent-Based Semiaqueous Cleaning.** This cleaning process cleaning seems to be a logical process step to pursue in some represents the mainstream of cleaning in the electronics in-
high-frequency applications. These applications require zero dustry due to its effectiveness in cle high-frequency applications. These applications require zero residue level on the boards to reduce parasitic capacitance parts. A solvent or solvents mixture that selectively attacks and crosstalk. The wafer bumping process, which tolerates no or dissolves flux residue constitutes the key element of the flux residue on the wafer after reflow, is also an important process. Unit operations include boards solvent immersion, product line that demands the necessity of cleaning. deionized water spray, and hot air dry. All are completed in

cleaning efficiency. Aqueous cleaning is the cleaning method are met can a solvent enter its rigorous final qualification that emits virtually no VOC under normal operation condithat emits virtually no VOC under normal operation condi-
tions. In the past problems associated with aqueous cleaning. Terpenes are among the first solvents that enter the qualitions. In the past, problems associated with aqueous cleaning Terpenes are among the first solvents that enter the quali-

prevented proliferation of this potentially "green" process for fication process. Originally, terpe prevented proliferation of this potentially "green" process for fication process. Originally, terpenes were natural essence
the following reasons: (a) It required a high nH (\sim 11) and oils isolated from pine tree saps (the following reasons: (a) It required a high pH (\sim 11) and oils isolated from pine tree saps (pinene) or from citrus peel
moderately high surfactant (5% to 10%) concentration to ings (limonene). Currently, most cleanin moderately high surfactant (5% to 10%) concentration to ings (limonene). Currently, most cleaning-grade terpenes are
work (b) chelating agents in the suractant made heavy met. from synthetic sources. Terpenes possess part work, (b) chelating agents in the suractant made heavy met-
als separation difficult if not impossible in the waste efflu-
lecular structure, and in fact they are the precursors of rosin als separation difficult, if not impossible, in the waste efflu-
ecular structure, and in fact they are the precursors of rosin
ent. and (c) it is difficult to reduce the high BOD (biological in the plants' biosynthesis pa ent, and (c) it is difficult to reduce the high BOD (biological oxygen demand, a method to quantify pollutants in water) in solvents for rosin-based fluxes. For the same reason, terpenes the effluent to meet regulatory requirements (46). With recent may not work on non-rosin-based fluxes, and other solvents advancement in the surfactant technology, new ''splittable'' must be used. The unsaturation in the rosin hydrocarbon surfactants (47) that improve or eliminate most previous chain makes them susceptible to air oxidation, resulting in problems have become a reality. The new surfactant performs both loss of cleaning capability and gum formation in the emulsification and cleaning functions at high pH and stops cleaning tool. Wong et al. (49) reported a detailed account on functioning and phase-separated in waste treatment tanks the decay of terpene solvents during cleaning. Other effective when pH is lowered by neutralization. This facilitates the re- solvents that have since emerged include esters, alcohols, and duction of BOD by surfactant removal, and thus it also allows miscellaneous solvents from which a full spectrum of applicaeffective separation of heavy metals from the waste stream tions are covered. Table 3 lists some common characteristics

Polymer Bumping. The advantages of polymer bumping in-
The cleaning action begins with an array of rotating rotating clude lead-free, flexible, and fatigue-resistant bumps, process brushes that are partially immersed in the cleaning solution simplicity, and no-clean. A surface treatment is applied to the of an aqueous cleaner. The wave-soldered circuit boards are device bondpads such that the conductivity between the adhe- carried under the brushes where saponification (hydrolysis) sives and bondpads interface can be maintained throughout occurs. The basic ingredients in the cleaning agent converts the product life cycle. This surface treatment uses palladium insoluble flux residue into a water-soluble form that dischloride to activate the aluminum bonpad surface first, fol- solves in water. After cleaning, high-pressure deionized walowed by a chelate immersion. The chelate Tiron (4,5-dihy- ter spray removes traces of ionics; this is then followed by droxy-1,3-disulfonic acid) forms a stable aluminum complex hot air dry to complete the cleaning cycle. droxy-1,3-disulfonic acid) forms a stable aluminum complex hot air dry to complete the cleaning cycle. Fine-pitch SMT
on the surface, thus preventing it from further oxidation. assembly that contains reflowed parts of smal assembly that contains reflowed parts of small geometry Tiron complex also catalyzes the cyclotrimerization of cyanate may not work with aqueous cleaning, due to the intrinsiesters; this further enhances the conductivity of the paste if cally high surface tension of water that prevents effective cyanate ester adhesive is used (34). Bumping operation in-
penetration. This is where the solvent-b penetration. This is where the solvent-based semiaqueous

has limited applications and is ideal for ceramics or wafer surface cleaning before bumping. This process may replace **CFC-Free Cleaning Processes** the standard plasma wafer cleaning that uses CF_4 or SF_6 ,

a conveyor-driven cleaning machine (46). Proper selection of Aqueous Cleaning. This cleaning process has its roots in the solvents that remove the residue without attacking other ele-
common household cleaning method of using soap in water as
the cleaning agent. Numerous modificatio

by standard precipitation techniques. $\qquad \qquad$ of solvents that have been evaluated as flux residue cleaner.

	Residue					
Solvents	Removability	Flash Point	Toxicity	Loading	Recyclability	Stability
Terpenes						
Limonene, pinene	Good	Low	Low	High	Low	Low
Esters						
DBE^a	Medium	High	Low	Low	High	Low
Lactates	Medium	Medium	Low	Medium	Low	High
Alcohols						
IPA ^b	Low	Very low	Medium	Low	Low	High
DAA^c	Low	Medium	Medium	Low	Medium	Medium
$THFA^d$	High	High	Low	High	High	Medium
Other						
D P G E ^e	Medium	High	Low	Medium	High	High
NMPf	Low	High	Medium	Low	High	High
Isoparaffin ^{g}	Low	High	Low	Low	High	High

Table 3. Characteristics of Solvents That Have Been Evaluated as Flux Residue Cleaners

^a DBE, dibasic esters of C4–C6 dicarboxylic acids.

^b IPA, isopropyl alcohol.

^c DAA, diacetone alcohol.

^d THFA, tetrahydrofurfuryl alcohol.

^e DPGE, dipropyleneglycol ethers.

^f NMP, *N*-methyl pyrrolidone.

^g Isoparaffin, C12–C18 isoalkane solvents.

ing with minimum usage of water and energy. Solvents are rizes the structure of a general waste reduction scheme (50). also recycled and reuse on-site. Replenishment is normally kept to a minimum by a closed-loop design (5) that reduces **Source Reduction** solvent loss. All chlorine-free organic solvents are either combustible or flammable, and strict fire and explosion-proof The most effective way of cutting waste production is source measures have to be taken into consideration when designing reduction. In fact, the US EPA narrowly defines pollution prethe tool and process for cleaning. vention as mainly measures taken to achieve source reduc-

and in post consumer products treatment. HAP, VOC, and product changes that reduce waste production. THI uses more
BOD are among the common terms associated with the as-
solder, uses materials that are not compatible (sold BOD are among the common terms associated with the as-
solder, uses materials that are not compatible (solder and ad-
sembly operation, that we are interested in monitoring and hesives), and produces solder sludges during sembly operation that we are interested in monitoring and controlling. A broader coverage is made here to discuss the elimination in solder alloys creates opportunities for new promonitoring of the sources of pollution in the assembly process cesses; polymer flip-chip technology (29) for example, is a di-
and the appropriate prevention methodology. The largest sec- rect result of this effort. Switch and the appropriate prevention methodology. The largest sector of US industry continues to invest far more in equipment cleaning processes into aqueous ozonated water cleaning (48) that treats polluted water and air, disposes of solid waste, is another example of source reduction and so on, than it invests in preventing the production of the pollutants. Pollution prevention can be achieved by increasing baths in the plating processes have achieved the total elimi-
efficiencies in the use of raw materials, water, energy, and nation of this dangerous chemical an efficiencies in the use of raw materials, water, energy, and nation of this d
other natural resources. The importance of increasing efficial problems
of the industry. other natural resources. The importance of increasing effi- in the industry.
ciency is particularly of great interest. The low efficiency of The mere conversion of CFC-based cleaning processes to ciency is particularly of great interest. The low efficiency of the current manufacturing processes can be represented by aqueous cleaning does not itself constitute a source reduction. the fact that it takes more than 4000 liters of water to convert On the contrary, the standard aqueous cleaning process proa 200 mm diameter raw wafer (11) into product devices. Re- duces more waste in a much diluted form than the solventducing water usage can effectively cut the pollution at source. based processes. Until there is technological innovation, such Pollution prevention is not merely making business sense, it as implementation of splittable surfactants in the aqueous also carries a legal obligation. The ruling of the US Environ- process, the aqueous cleaning only serves to change the form mental Protection Agency (US EPA) as specified in SARA (Su- of waste rather than to reduce it. perfund Amendments and Reauthorization Act), Title III, Sec- Recent incidents of HFC-123 (hydrofluorochlorocarbontion 313, mandates companies to report publicly on certain 123) leaks prompt further attention on the toxicity of the CFC chemical releases to the environment. Waste reduction is the substitutes (52). The researchers concluded that HFC-123 is

Most of the solvent-based systems perform effective clean- be zero release to meet no reporting criteria. Figure 5 summa-

tion. In the electronics assembly lines, this can be done by using less volatile cleaning agents or by eliminating cleaning **POLLUTION PREVENTION EXECUTION** entirely. Process changes (such as shifting underfill operation to ACF interconnection) that do not require underfill fall into Pollution sources exist during assembly, tool maintenance, this category. Conversion of THI to SMT is an example of that treats polluted water and air, disposes of solid waste, is another example of source reduction on PFC. Implementa-
and so on, than it invests in preventing the production of the tion of cyanide-free electrolytic (51)

first step toward minimizing releases. The final goal should safe as long as exposure does not exceed the 50 ppm limit.

Figure 5. A tiered structure of a general waste reduction scheme.

control as a means of achieving source reduction. Both reac- ics industry's effort in cutting waste production. Due to the tive and predictive control (53) can be followed after the moni- short product life cycles of this industry, the long-term prostoring systems detect an emission. Predictive control forecasts pects of components reuse is more challenging than in other an anticipated emission and takes appropriate measures to traditional industries. The automotive industry, for example, correct the situation before the pollution actually occurs, and can reuse the spare spark plugs it produced a few decades therefore this is a preferable technique. Examples of applica- back without encountering problems, while today's personal tions of predictive control in the assembly lines include pro- computers can barely reuse any inventoried disk drives that grammed solvent cleaner-scrubber control and loading-sensi- are more than a few years old. The comp tive plating bath–sewer discharge management. Key toxic printed wiring board (PWB) products is a deterrent that de-
pollutants that are commonly used in the electronics industry fies cost-effective reclamation attempts. On pollutants that are commonly used in the electronics industry fies cost-effective reclamation attempts. On the other hand, include methyl ethyl ketone, tetrachloroethylene, trichloro-current multilaver PWBs contain an ave ethylene, dichloromethane, xylene, glycol ethers, cyanide, gold (54), which is equivalent to 10 times the gold concentra-
chromium, lead, and mercury. Source reduction of all these tion in a high-grade gold ore: this can m chromium, lead, and mercury. Source reduction of all these tion in a high-grade gold ore; this can make gold reclamation pollutants can be achieved by applying proper monitoring-
from these products a profitable business. pollutants can be achieved by applying proper monitoring-
control methodology. Table 4 shows some common techniques
between pollution associated with the reclamation and effeccontrol methodology. Table 4 shows some common techniques between pollution associated with the reclamation and effec-
for monitoring the specific pollutants that are related to the tive resource recovery is an issue that electronics industry. $\qquad \qquad \text{assessed.}$

original form (reuse) or in the degenerated form (reclama-

^a VOCs, volatile organic compounds.

b BOD, biological oxygen demand.

^c AA, atomic absorption spectroscopy.

^d ICP, inductive-coupled plasma spectroscopy.

^e ISE, ion-selective electrode.

^f IC, ion chromatography.

This example illustrates the importance of monitoring and tion). It represents the major unexplored area of the electronare more than a few years old. The complexity of assembled current multilayer PWBs contain an average of ~ 600 ppm of tive resource recovery is an issue that needs to be carefully

Iji and Yokoyama (54) of NEC, Japan, developed a com-
 Recycling plete recycling scheme that involved breaking down the Recycling involves using the material more than once in its mounted PWBs into separate components, all by physical original form (reuse) or in the degenerated form (reclama- methods, and reused all recovered materials. Thi represents the most environmentally sound PWB recycling process to date. Since there was no generation of toxic fumes or waste water, the recycling carries little impact on the environment. However, the presence of a trace amount of lead, approximately 200 ppm, in the recovered resin–glass fiber mixture may limit its reuse as a filler in general applications. This result indicates that the use of lead-free solder or polymer interconnects should be at the top of the product life cycle if total product recyclability is desirable.

PRODUCT END-OF-LIFE MANAGEMENT

The concept of "product take back" (55) clearly identifies that manufacturers bear the final responsibility of the products they produced. An integrated model that addresses issues from product design to end-of-life (EOL) (3) product manage-

Activity	Operation	Design Implications	$\rm Cost$	Environmental Impact	
Resale	Recover and sale	Durability, spares	Low	Low	
Remanufacture	Recover and restore	Cleanable, spares	Medium	$_{\rm Low}$	
Upgrade	Improve functionality	Modular design	Uncertain	Low	
Recycling	Disassemble material	Compatibility, disassembility	High	Medium	
Scrap	Landfill or incinerate	Minimum value and volume	Low	High	

Table 5. A Summary of Products End-of-Life Activities and the Impacts

ment has been discussed. Low and Williams (3) used the fol- 11. *National Technology Roadmap for Semiconductors Technology Needs,* 1997 ed., San Jose, CA: Semiconductor Industry Associa-
 Continuo unormals requaling and seroning Toble 5 summarizes tion, 1997. facture, upgrade, recycling, and scraping. Table 5 summarizes

Resale, remanufacture, and upgrade reuse the bulk of the supply and environment products and therefore are more environmentally sound than *gen*, **42** (1–2): 64–66, 1992. products and therefore are more environmentally sound than *gen,* 42 (1–2): 64–66, 1992.

products and scraping. Major electronics companies realize 13. M. Yamaguchi et al., Non halogen/antimony flame retardent sysrecycling and scraping. Major electronics companies realize 13. M. Yamaguchi et al., Non halogen/antimony flame retardent sys-
the importance of developing products that can be reusable tem for high end IC package, Proc. 4 the importance of developing products that can be reusable the for high end IC package, *Proc. 47th Electron. Compon. Tech*-
and recyclable. Motorola, for example, started tagging its nol., 1997, pp. 1248–1253.
plastic com plastic components so that when they are disassembled at 14. G. Milad, Surface finishes: Metallic coatings over nickel over cop-
EQL, proper material classification can be identified and recy- per, Proc. Surface Mount Int. EOL, proper material classification can be identified and recy-
also accordingly Technol, and retain *Surfactive colution.* 1996, pp. 794–796. 1996, pp. 794–796. cled accordingly. Tagging is a simple and yet effective solution to the contamination problem. Cross-contamination of plas-
tics has been a major problem that has discouraged recycling ogies in the soldering process. Innovation of wave solder machine tics has been a major problem that has discouraged recycling. ogies in the soldering process. Innovation of wave solder machine
Most of the plastics are not compatible and thus require clase equipment, Proc. Tech. Program, Most of the plastics are not compatible and thus require clas- equipment, $P_{063-971}$ sification before recycling. The impact of contamination is re-
flected by the low recycling volume of the plastics Oply 2% to 16. U. Ray et al., Solderability and thermal stability of azole corro-S% of all plastics that have been produced are recycled today.

This number can increase significantly if measures such as
 $\frac{p}{2}$, $\frac{423-35}{p}$.

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